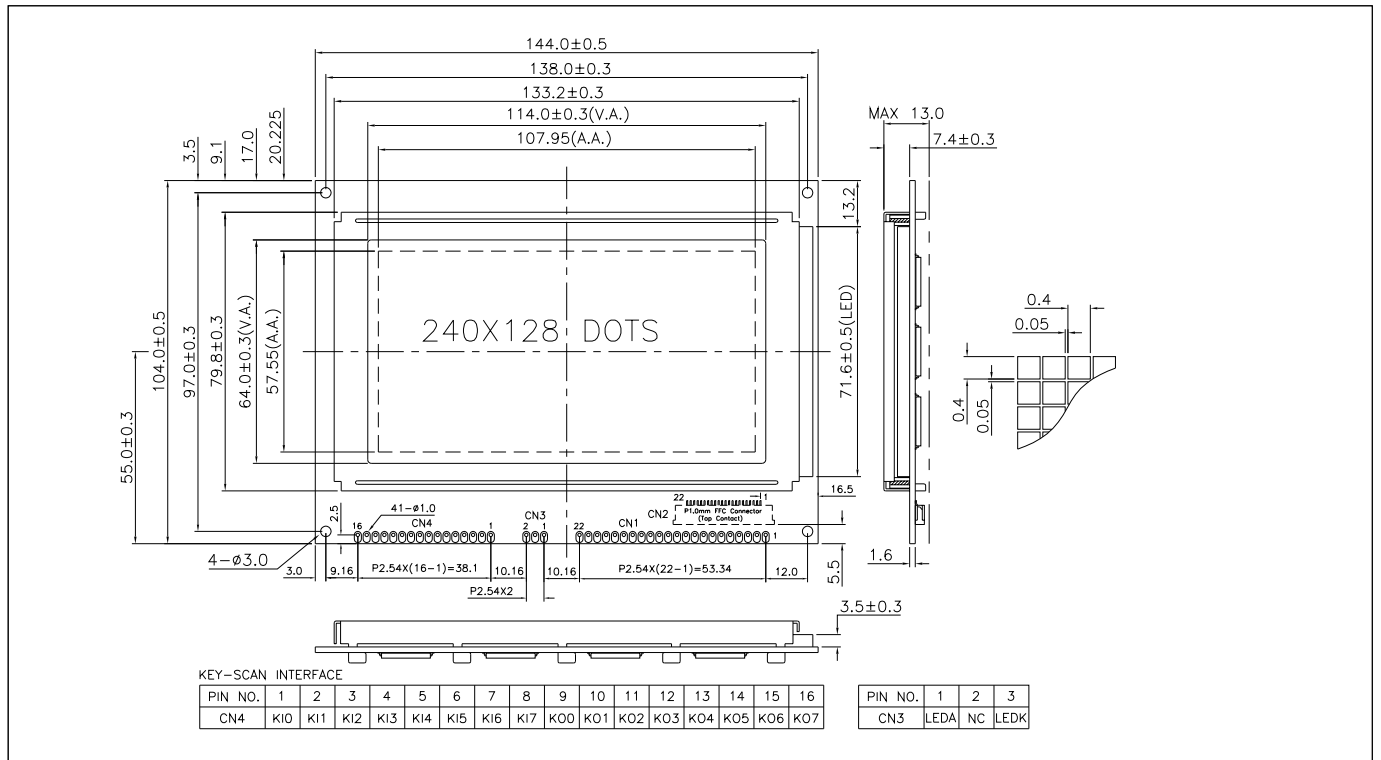


LG2401289-DW

4.8", 240 x 128 dots + white led backlight + key-scan, 8-bit parallel, 5V, built-in Chinese font



ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min.	Max.	Unit
Supply Voltage(Logic)	$V_{DD} - V_{SS}$	-0.3	5.5	V
Supply Voltage(LCD)	$V_{DD} - V_O$	-0.3	25.0	V
Input Voltage	V_I	-0.3	$V_{DD} + 0.3$	V
Operating Temp.	T_{opr}	-20	70	°C
Storage Temp.	T_{stg}	-30	80	°C

MECHANICAL DATA

Item	Nominal Dimensions	Unit
Module Size (W x H x T)	144.0 x 104.0 x 13.0	mm
Viewing Area (W x H)	114.0 x 64.0	mm
Dot Pitch (W x H)	0.45 x 0.45	mm
Dot Size (W x H)	0.40 x 0.40	mm
Weight	Approx. 185	g

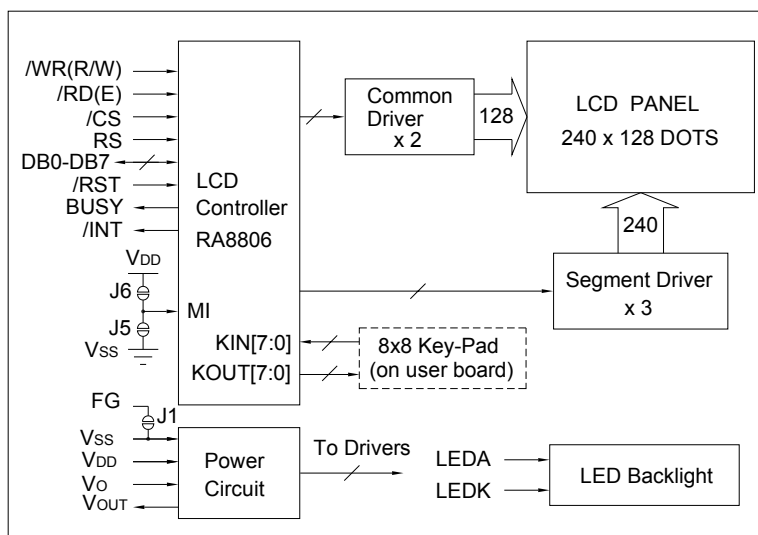
ELECTRICAL CHARACTERISTICS ($V_{DD}=5V$)

Item	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Input High Voltage	V_{IH}	--	$0.8V_{DD}$	--	V_{DD}	V
Input Low Voltage	V_{IL}	--	0	--	$0.2V_{DD}$	V
Output High Voltage	V_{OH}	--	$V_{DD}-0.4$	--	V_{DD}	V
Output Low Voltage	V_{OL}	--	0	--	0.4	V
Supply Current	I_{DD}	$V_{DD} = 5.0V$	--	10.0	15.0	mA
LCD Driving Voltage	$V_{DD} - V_O$	$T_a=25^\circ C$	--	18.2	--	V

PIN CONNECTIONS (CN1/CN2)

Pin	Symbol	Level	Function
1	FG	--	Frame ground
2	VSS	0V	GND
3	VDD	5V	Power supply for logic
4	V_O	--	Operating voltage for LCD
5	/WR(R/W)	H/L	/WR for 80 MPU, R/W for 68 MPU
6	/RD(E)	H/L	/RD for 80 MPU, E for 68 MPU
7	/CS	L	Chip enable signal. Active "L".
8	RS	H/L	L : Data H : Instruction code
9	BUSY	H/L	Busy signal output
10	/RST	L	Reset signal. Active "L".
11	DB0	H/L	Data bus
12	DB1	H/L	
13	DB2	H/L	
14	DB3	H/L	
15	DB4	H/L	
16	DB5	H/L	
17	DB6	H/L	
18	DB7	H/L	
19	INT	H/L	Interrupt signal output
20	V_{OUT}	-14V	Output voltage for LCD driving
21	LEDA	5V	Power supply for LED backlight
22	LEDK	0V	

BLOCK DIAGRAM



LED BACKLIGHT SPECIFICATIONS ($T_a=25^\circ C$)

Item	Symbol	Typ.	Max.	Unit
Forward Voltage	V_f	2.9	3.0	V
Forward Current	I_f	105	--	mA
LED Color		White		